IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s)	Larsen, et al.	Examiner	Iwashko, Lev
Serial No.	10/694,302	Group Art No.	2186
Filed	October 27, 2003		
For	Method and Program Product for Avoiding Cache Congestion by Offsetting Addresses While Allocating Memory		

Mail Stop: AMENDMENT
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

DECLARATION
UNDER 37 C.F.R. § 1.131

We, Douglas Vincent Larson, Richard Glen Fowles, Blaine Douglas Gaither, Benjamin Daniel Osecky note first named inventor on office action is not on the disclosure>, declare as follows. If no signature is included herein for one or more of the named inventors in the previous sentence, see paragraph 12 of this Declaration.

1.   We are named inventors for U.S. Patent Application No. 10/694,302 filed on October 27, 2003 (hereinafter the "Application").

2.   Hewlett Packard Development Company L.P. ("HP") owns the entire right, title, and interest in and to the Application. The assignment from the named inventors to HP is recorded at reel <TBD>, frame <TBD>.

3.   We have reviewed and understand the specification and claims in the Application.

4.   We have reviewed and understand the contents of the Office action dated October 26, 2005, and the references cited therein. We have reviewed and understand in particular the paper by Chavarria-Miranda et al. entitled "An Evaluation of Data-Parallel Compiler Support for Line-Sweep Applications" (hereinafter Chavarria-Miranda).

5.   This declaration is being presented under 37 C.F.R. § 1.131 to establish invention prior to the effective date of Chavarria-Miranda (published April 2003) or conception of the invention prior to the effective date of Chavarria-Miranda

(published April 2003) coupled with due diligence prior to that date to a subsequent reduction to practice or to the filing of this Application.

6.   Attached hereto as Exhibit A (1 page) is page 5 of an Invention Disclosure Form ("IDF"). The IDF was completed by at least one named inventor, signed by the named inventors on January 30, 2003, and submitted to the HP Legal Department on or before January 31, 2003.

7.   Embodiments shown in Figures 3-5 of the Application were conceived or conceived and reduced to practice at least as early as January 31, 2003. Email discussions of the concept occurred on or before September 19, 2002 (the "September 19 Descriptions") (copy not included herein). The September 19 Descriptions identify at least one complete conception of a system and method for allocating memory as claimed.

8.   A complete conception or conception and reduction to practice of at least one embodiment of a system and method for allocating memory is identified in the IDF. A complete conception or conception and reduction to practice of at least one embodiment of a system and method for processing a signal as claimed was made at least as early as January 31, 2003.

9.   A complete conception or conception and reduction to practice of at least one embodiment of a system and method for processing a signal is identified in the September 19 Descriptions. A complete conception or conception and reduction to practice of at least one embodiment of a system and method for processing a signal as claimed was made at least as early as September 19, 2002.

10.   From January 31, 2003, through October 27, 2003, the inventors diligently worked on preparing and finalizing materials for the Application. The Application was filed on October 27, 2003.

11.   All acts relied on were carried out within the United States.

12.   Fewer than all inventors may be making this Declaration under 37 C.F.R. § 1.131 if the remaining inventor(s) could not be located or reached before this Declaration was signed and/or filed. Multiple attempts were made to locate, communicate with, or otherwise reach all named inventors prior to the signing and/or filing of this Declaration.

Docket: 200309576-1

I/we hereby declare that all statements made herein of my/our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and such willful false statements may jeopardize the validity of the Application or any patent issued thereon.

Respectfully Submitted,

Date   19 Jan 2006

By _Douglas V. Larson_

Douglas Vincent Larson

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Date _23 January 2006_   By _Richard Glen Fowles_

Richard Glen Fowles

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Date 2-9-06

By *Blaine Douglas Gaither*
Blaine Douglas Gaither

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Date ___2/9/2006___   By ___Benjamin Daniel Osecky___
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